

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	195	(voltage adj regulator) same (programmable adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:18
L2	0	1 and (on adj chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:18
L3	117	1 and chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:34
L4	2	"6683767".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:34
L5	1	4 and (measur\$5 or detect\$4 or monitor\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:48
L6	2	"6744242".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:49
L7	13	(chip adj2 external adj4 voltage) and (voltage adj regulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:52
L8	2037	(chip with regulat\$4) and (voltage adj regulator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:52
L9	133	8 and (circuit adj3 operating adj3 voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:53

L10	6	9 and (measur\$4 or detect\$4 or monitor\$4) same (parameter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/13 16:54
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